

<b>Supp. Form PTO-1449</b> (modified)  List of Patents and Publications For Applicant's Information Disclosure Statement  (Use several sheets if necessary)		<b>ATTY. DOCKET NO.</b> AMDI:115\HON		<b>SERIAL NO.</b> 10/085,903			
		<b>APPLICANT</b> Andy Wei et al.		<b>CERTIFICATE OF FACSIMILE TRANSMISSION</b> 37 C.F.R. § 1.6(d)  I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office (Fax No. (703) 872-9306), on the date below:  Date of transmission: <u>April 15, 2004</u> Signature: <u>[Signature]</u> Printed Name: <u>Timothy M. Honeycutt</u>			
		<b>FILING DATE</b> February 28, 2002		<b>GROUP</b> 2823			
<b>U.S. PATENT DOCUMENTS</b>							
<b>EXAM INIT.</b>	<b>REF. DES.</b>	<b>DOCUMENT NUMBER</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUB CLASS</b>	<b>FILING DATE IF APPROPRIATE</b>
<b>FOREIGN PATENT DOCUMENTS</b>							
<b>EXAM INIT.</b>	<b>REF. DES.</b>	<b>DOCUMENT NUMBER</b>	<b>DATE</b>	<b>COUNTRY</b>	<b>CLASS</b>	<b>SUB CLASS</b>	<b>TRANSLATION</b>
<b>OTHER ART</b>							
<b>EXAM INIT.</b>	<b>REF. DES.</b>	<b>AUTHOR; TITLE; JOURNAL; VOLUME; DATE; PERTINENT PAGES</b>					
<u>Lee</u>	<u>A1</u>	<u>Stanley Wolf and Richard N. Tauber; Silicon Processing for the VLSI Era, Volume 2 - Process Integration; 1990; pp. 298-299</u>					
<b>EXAMINER</b> <u>Kevin Mary Lee</u>				<b>DATE CONSIDERED</b> <u>6/16/04</u>			
<small>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							

Information Disclosure Statement--PTO-1449 (Modified)